

Exploring Artificial Intelligence-based Optimizations for Designs Involving Active and Passive Components: Theories, Methods, Implementations, and Applications

Lida Kouhalvandi¹, Sercan Aygun², Abu Kaisar Mohammad Masum², Ladislau Matekovits^{3,4,5,6}

¹Department of Electrical and Electronics Engineering, Dogus University, Istanbul, Turkiye, lida.kouhalvandi@ieee.org

²School of Computing and Informatics, University of Louisiana at Lafayette, USA, sercan.aygun@louisiana.edu, abu-kaisar-mohammad.masum1@louisiana.edu

³Department of Electronics and Telecommunications, Politecnico di Torino, Turin, Italy,

⁴Department of Measurements and Optical Electronics, Politehnica University Timisoara, Timisoara, Romania

⁵Istituto di Elettronica e di Ingegneria dell'Informazione e delle Telecomunicazioni, National Research Council, Turin, Italy, ladislau.matekovits@polito.it

⁶American Romanian Academy of Arts and Sciences, Citrus Heights, CA 95621, USA

Abstract: The cooperative presence of active and passive devices, such as amplifiers and antennas, is well-settled in wireless communication systems. Due to the complexity of such systems, their design requires the use of advanced methodologies along with optimization methods. Recently, Artificial Intelligence (AI) has proved its effectiveness in various fields and applications, leading to the acceleration of design and optimization processes. Therefore, this paper aims to present various methodologies used in the shared design and optimization of amplifiers and antennas using the AI method in terms of optimal structure arrangement and configuration sizing. In this paper, the practical procedure for implementing AI for the design and optimization of large-scale projects is described. In short, the use of this method (i.e., AI) requires the collaboration of electronic design automation (EDA) and numerical analyzer tools, simultaneously leading to the creation of an automated environment. Readers of this study will gain an overview of AI implementation for high-dimensional projects. Depending on the need determined by the specific project, one or more of the methodologies described in this paper may be used.

Keywords: Artificial intelligence (AI), automation, antenna, neural networks (NN), optimization, power amplifier (PA).

(Title and abstract in Romanian)

Explorarea Optimizarilor Bazate pe Inteligență Artificială pentru Proiecte care Implică Componente Active și Pasive: Teorii, Metode, Implementari și Aplicații

Abstract: Prezența cooperativă a dispozitivelor active și pasive, cum ar fi amplificatoarele și antenele, este bine stabilită în sistemele de comunicații fără fir. Datorită complexității acestor sisteme, proiectarea lor necesită utilizarea unor metodologii avansate împreună cu metode de optimizare. Recent, Inteligența Artificială (IA) și-a dovedit eficacitatea în diverse domenii și aplicații, conducând la accelerarea proceselor de proiectare și optimizare. Prin urmare, această lucrare își propune să prezinte diverse metodologii utilizate în proiectarea și optimizarea comună a amplificatoarelor și antenelor folosind metoda IA, în ceea ce privește aranjamentul optim al structurii și dimensionarea configurației. În această lucrare, este descrisă procedura practică pentru implementarea IA în proiectarea și optimizarea proiectelor de mare anvergură. Pe scurt, utilizarea acestei metode (adică IA) necesită colaborarea dintre unelele de automatizare a proiectării electronice (EDA) și cele de analiză numerică, conducând simultan la crearea unui mediu automatizat. Cititorii acestui studiu vor obține o imagine de ansamblu asupra implementării IA pentru proiecte de dimensiuni mari. În funcție de necesitatea determinată de proiectul specific, una sau mai multe dintre metodologiile descrise în această lucrare pot fi utilizate.

Keywords: Inteligența Artificială (IA), automatizare, antene, rețea neurale (NN), optimizare, amplificator de putere.

I. INTRODUCTION

With the development of communication systems, data traffic is increasing exponentially, resulting in various new issues and drawbacks. These challenges can be formulated by considering the necessity for mobility issues [1], big system capacity [2], energy consumption [3], performance metrics [4], and so on. Hence, the implementation of powerful and suitable optimization methods will help in enhancing the overall performance of systems. Recently, the Artificial Intelligence (AI) method has proven its novelty in solving high-dimensional designs, leading to improved performance of devices in terms of targeted output specifications.

In [5], the Machine Learning (ML) method is employed for reducing the calculation time, leading to predicting the path loss model in indoor scenarios in a much faster way.

The ML method is employed in [6] for an analog radio-over-fiber link, leading to enhancing receiver sensitivity. The combination of a Reinforcement Learning (RL) algorithm and a Deep Q-Neural Network (DQN) is employed in [7] for managing congestion control under a User Datagram Protocol environment. The RL is also executed in [8] to achieve robust and low-overhead security in the reconfigurable Power Amplifier (PA) designs. The Federated Learning (FL) is another learning framework that is executed over wireless networks. In [9], the FL method is used for reducing the total energy consumption over all devices. Kee et al. in [10] present detailed information about the various types of ML techniques for channel coding and describe the importance of this method for their problem.

AI methods have also been used recently in the field and domain of PAs. In [11], a Neural Network (NN) is employed for reducing baseband intermodulation distortion, which is important for Digital Pre-Distortion (DPD). A phase-normalized recurrent neural network is employed in [12] for linearizing PAs in high-bandwidth communication systems by focusing on the memory effects. Since the DPD issue is significant in PA designs, a Deep Neural Network (DNN) is executed to minimize the computational complexity and memory footprint. In [13], the DPD issue is studied with the help of Recurrent Neural Network (RNN) for enhancing the overall performance of PA.

Alike the use of AI in the PA designs, it is also executed in the domain of passive devices as antenna designs. In [14], the NN is used for analyzing and evaluating the Specific Absorption Rate (SAR) that results in reducing test time costs. The Convolutional Neural Network (CNN) is used in [15] in the field of adaptive broadband digital beamforming for the generation of beamforming Weights. With the help of synthetic NN, in [16], a reconfigurable metasurface is presented, which solves the limit of metasurfaces' intrinsic scale.

By reviewing the up-to-date references, it is observed that *the AI and ML methods are widely used in designing and optimizing wireless systems, PAs, and antennas for various goals*. This study is devoted to presenting the use of AI in modeling and sizing the high-dimensional PA and antenna designs, leading to improving each of their overall performances. The remainder of this chapter is as follows: Section II describes the suitable generated environment for implementing AI, leading to the design and optimize active and passive devices. Section III provides a summary of some designed and optimized PAs and antennas through the AI method. Finally, Section IV concludes this paper.

II. ENVIRONMENT CREATION FOR AUTOMATED OPTIMIZATION PROCESS

The AI methods are typically implemented in interface-oriented numerical analyzer environments such as Octave, MATLAB, Python, R, etc., in which various ML libraries are

included [17]. From another point of view, active and passive devices such as PA and antenna are designed in the

Electronic Design Automation (EDA) tools, software tools suitable for designing integrated circuits and printed circuit boards [18]. Some of the EDA tools are OrCAD, Cadence, Cadence Design Systems, Ansys, Keysight Technologies, SPICE simulator, etc. Hence, implementing AI methods for electronic computer-aided design requires a connection between two different environments, as numerical analyzers and EDA tools. This requires a cross-platform design; otherwise, human inference is needed. Figure 1 presents the general overview of the various environment connections between EDA tools and numerical analyzers, leading to the design and optimizing diverse active and passive devices.

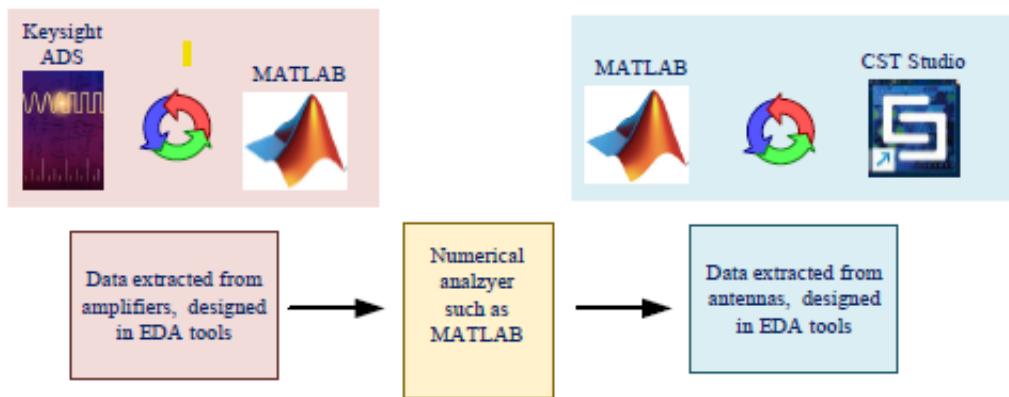


Fig. 1. Connection between EDA tools and numerical analyzers.

The main core of the presented environment is the numerical analyzer that manages all the optimization processes in which AI methods are employed. Here, the numerical analyzer is operating in the forward, and EDA tools are operating in the background prompted by the numerical analyzers [19].

III. PRACTICAL IMPLEMENTATION OF AI FOR DESIGNING AND OPTIMIZING ACTIVE AND PASSIVE CIRCUITS

After constructing an automated environment between EDA tools and numerical analyzers, the design and optimization process for PA and antenna circuits can be executed. Firstly, this section is devoted to presenting the procedures leading to constructing the initial structures of PAs and antennas. Afterward, the methodologies for obtaining the optimal

design parameters are explained.

A. Generating the initial structure of PA and antenna

Configuring the initial structures of active and passive designs is not straightforward, and mostly, it depends on the designers' experience. For instance, the general structure of any PA includes input and output matching networks (MNs) with the substitution of transistor between these networks as shown in Figure 2. Hence, configuring these MNs is complex, and designers aim to construct the various networks with advanced methodologies.

To tackle this problem, there are various reported methods as below:

- Simplified Real Frequency Technique (SRFT),
- Classification DNN, • Bottom-Up Optimization (BUO),
- Top-Down Optimization (TDO).

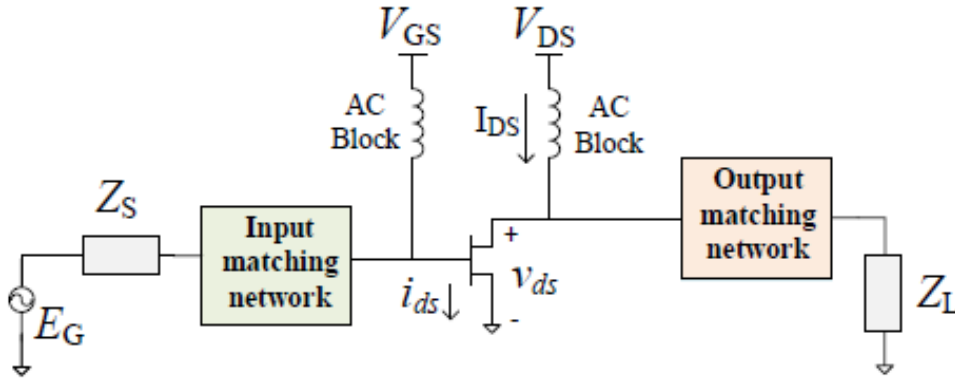


Fig. 2. General structure of any PA.

The SRFT can be a successful candidate for presenting the initial configurations for the input and output MNs by inserting the load-pull data to this method [20]. This method results in various MN typologies that vary in the number of components, including either capacitor/inductor or transmission lines (TLs). These numbers are defined by the numerator polynomial as defined in Eq. (1) in which the starting matrix (i.e., $h_0 = [\mp 1 \ 0]$) has the size of $1 \times m$, for all $m \geq 3$.

$$h(\lambda) = h_1 \lambda^n + h_2 \lambda^{(n-1)} + \dots + h_n + 1 \quad (1)$$

By differing the number of m in the matrix of the SRFT method, various configurations are achieved. Hence, deciding on the optimal configuration is critical. For this case, in [21], a methodology based on the classification DNN is presented, leading to the prediction of the optimal MNs based on the performance of the transistor model. Figure 3 presents the

general structure of the proposed classification DNN presented in [21] in which the input specifications are phase distortion (AM/PM), amplitude distortion (AM/AM), drain efficiency (η_D), and power gain (G_p). More details regarding the construction of classification DNN are also presented in [22].

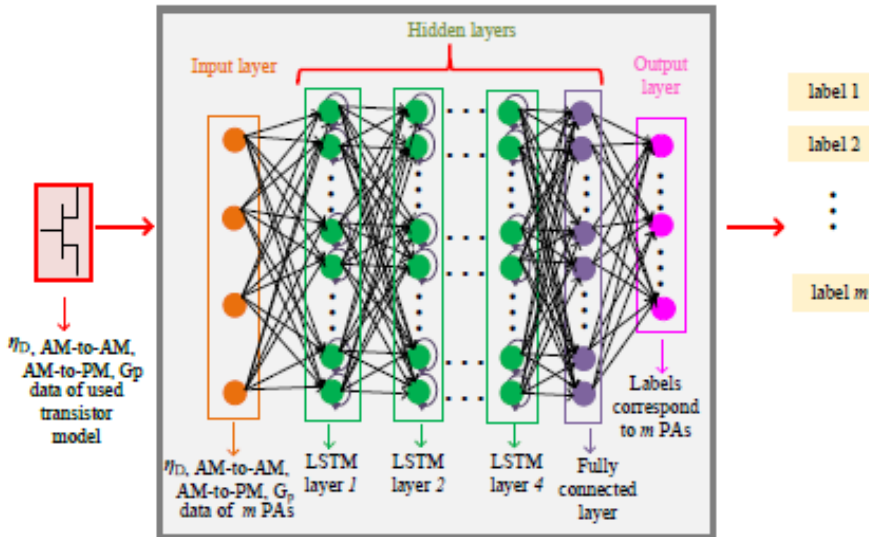


Fig. 3. Classification DNN presented in [21] for determining the optimal MNs for the PA design.

The BUO is another method leading to configuring system-level and/or circuit-level designs by decomposing into sub-blocks. This methodology starts with the lowest level and is increased sequentially and hierarchically up to achieve suitable output performance. On the other hand, the TDO method can be employed first to analyze the whole system and then break the entire system into small pieces [23]. Figure 4 presents the general flowchart of two optimizations leading to the configuration of the initial structure of active and passive designs.

The BUO and TDO methods are also employed in configuring antennas as well. Figure 5 presents the BUO method in which the optimization process starts with one TL and then is increased in the number of TLs, leading to the configuration of the antenna's structure. An example of demonstrating the TDO method is presented in [24]. Opposite to the BUO method, the TDO method is used to configure the structure of biomedical Multiple Input Multiple Output (MIMO) antenna in which firstly the biomedical layers are constructed and afterward the whole shape of the MIMO antenna is configured. Finally, the small parts of the MIMO structure are settled down (See Figure 6).

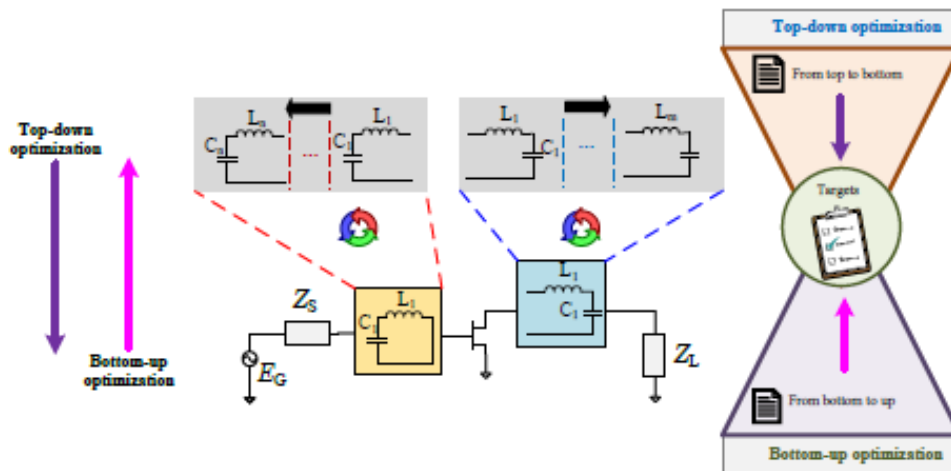


Fig. 4. A general view of process performance of BUO and TDO methods for designing a PA [19].

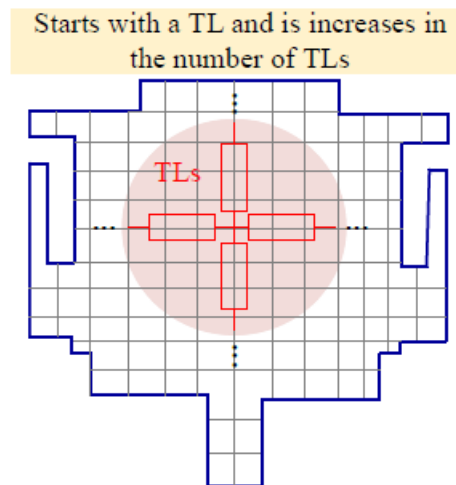


Fig. 5. Process of BUO in configuring the antenna structure.

B. Sizing of design parameters included in the configuration

The NN is a flexible structure for which the input layer and output layers can be defined by the designer. Hence in various recently published papers, AI methods are employed for sizing the design parameters of active and passive devices. Figure 7 shows the general structure of DNN where Long Short-Term Memory (LSTM) layers are used. By training accurate DNN, the determined specifications at the output layer are predicted for any given specifications in the input layer.

IV. CONCLUSION

Designing and optimizing amplifiers as active devices, and antennas as passive devices are not straightforward; additional efforts are required. The AI methods prove their effectiveness in designing high-dimensional designs leading to present high-performance devices. For this case, this paper is devoted to presenting the various AI methods applied for designing and optimizing amplifiers and antennas. Implementation of AI methods for these designs requires the collaboration of EDA tools and numerical analyzers. Hence, firstly, a suitable environment must be created. Afterward, the optimal configuration of the PA and antenna can be structured through SRFT, BUO, TDO, and/or classification neural networks. Finally, neural networks (preferably regression networks) can be trained to optimize the targeted specifications of designs and achieve the optimal design parameters. By using the AI methods, human inference is reduced, leading to fewer errors, and the time consumed for optimizing these designs is reduced.

REFERENCES

- [1] M. Xie, D. Pi, C. Dai, and Y. Xu, "An asymmetric dominated multiobjective optimization algorithm for reducing energy consumption of wsn operation," *IEEE Sensors Journal*, vol. 24, no. 14, pp. 23075–23087, 2024. DOI: [10.1109/JSEN.2024.3409459](https://doi.org/10.1109/JSEN.2024.3409459).
- [2] Y. Mao, X. Yang, L. Wang, *et al.*, "A high-capacity mac protocol for uav-enhanced ris-assisted v2x architecture in 3-d iot traffic," *IEEE Internet of Things Journal*, vol. 11, no. 13, pp. 23711–23726, 2024. DOI: [10.1109/JIOT.2024.3387997](https://doi.org/10.1109/JIOT.2024.3387997).
- [3] K. Das, N. N. Devi, and S. Moulik, "Eada: Energy-aware adaptive duty-cycle adjustment in superframe for ieee 802.15.6-based wireless body area networks," *IEEE Sensors Letters*, vol. 8, no. 8, pp. 1–4, 2024. DOI: [10.1109/LSSENS.2024.3432161](https://doi.org/10.1109/LSSENS.2024.3432161).
- [4] M. I. Waly, J. Smida, M. Bakouri, *et al.*, "Optimization of a compact wearable lora patch antenna for vital sign monitoring in wban medical applications using machine learning," *IEEE Access*, vol. 12, pp. 103860–103879, 2024. DOI: [10.1109/ACCESS.2024.3434595](https://doi.org/10.1109/ACCESS.2024.3434595).
- [5] Y. H. Santana, R. Martinez Alonso, G. Guillen Nieto, L. Martens, W. Joseph, and D. Plets, "5g mmwave network planning using machine learning for path loss estimation," *IEEE Open Journal of the Communications Society*, vol. 5, pp. 3451–3467, 2024. DOI: [10.1109/OJCOMS.2024.3405742](https://doi.org/10.1109/OJCOMS.2024.3405742).
- [6] G. Hao Thng and S. Mikki, "A machine learning aided reference-tone-based phase noise correction framework for fiber-wireless systems," *IEEE Transactions on Machine*

Learning in Communications and Networking, vol. 2, pp. 888–903, 2024. DOI: [10.1109/TMLCN.2024.3418748](https://doi.org/10.1109/TMLCN.2024.3418748).

[7] A. R. Andrade-Zambrano, J. P. A. León, M. E. Morocho-Cayamcela, L. L. Cárdenas, and L. J. de la Cruz Llopis, “A reinforcement learning congestion control algorithm for smart grid networks,” *IEEE Access*, vol. 12, pp. 75072–75092, 2024. DOI: [10.1109/ACCESS.2024.3405334](https://doi.org/10.1109/ACCESS.2024.3405334).

[8] J. Xu, Y. Shen, J. Deng, E. Chen, and V. Chen, “A reinforcement-learning-assisted power amplifier for rf fingerprint generation in 65 nm cmos,” *IEEE Transactions on Circuits and Systems I: Regular Papers*, vol. 71, no. 7, pp. 3050–3063, 2024. DOI: [10.1109/TCSI.2024.3390694](https://doi.org/10.1109/TCSI.2024.3390694).

[9] O. Marnissi, H. El Hammouti, and E. H. Bergou, “Adaptive sparsification and quantization for enhanced energy efficiency in federated learning,” *IEEE Open Journal of the Communications Society*, vol. 5, pp. 4307–4321, 2024. DOI: [10.1109/OJCOMS.2024.3425531](https://doi.org/10.1109/OJCOMS.2024.3425531).

[10] H. Lim Meng Kee, N. Ahmad, M. Azri Mohd Izhar, K. Anwar, and S. X. Ng, “A review on machine learning for channel coding,” *IEEE Access*, vol. 12, pp. 89002–89025, 2024. DOI: [10.1109/ACCESS.2024.3412192](https://doi.org/10.1109/ACCESS.2024.3412192).

[11] J. Ren, A. Song, Z. Xu, and H. Hu, “An integrated scheme of fir and augmented real-valued time-delay neural network of harmonic cancellation digital predistortion model for high-frequency power amplifier,” *IEEE Microwave and Wireless Technology Letters*, vol. 34, no. 7, pp. 951–954, 2024. DOI: [10.1109/LMWT.2024.3392811](https://doi.org/10.1109/LMWT.2024.3392811).

[12] A. Fischer-Bühner, L. Anttila, M. Dev Gomony, and M. Valkama, “Recursive neural network with phase-normalization for modeling and linearization of rf power amplifiers,” *IEEE Microwave and Wireless Technology Letters*, vol. 34, no. 6, pp. 809–812, 2024. DOI: [10.1109/LMWT.2024.3393859](https://doi.org/10.1109/LMWT.2024.3393859).

[13] Q. Zhang, C. Jiang, G. Yang, R. Han, and F. Liu, “Block-oriented recurrent neural network for digital predistortion of rf power amplifiers,” *IEEE Transactions on Microwave Theory and Techniques*, vol. 72, no. 7, pp. 3875–3885, 2024. DOI: [10.1109/TMTT.2023.3337939](https://doi.org/10.1109/TMTT.2023.3337939).

[14] M.-Z. Li, Z.-G. Liu, Z.-P. Chen, and W.-B. Lu, “Fast algorithm for noninvasive sar prediction based on artificial neural networks,” *IEEE Transactions on Antennas and Propagation*, vol. 72, no. 7, pp. 6139–6144, 2024. DOI: [10.1109/TAP.2024.3411151](https://doi.org/10.1109/TAP.2024.3411151).

[15] C. Xue, H. Zhu, S. Zhang, Y. Han, and W. Sheng, “Broadband beamforming weight generation network based on convolutional neural network,” *IEEE Geoscience and Remote Sensing Letters*, vol. 21, pp. 1–5, 2024. DOI: [10.1109/LGRS.2024.3403808](https://doi.org/10.1109/LGRS.2024.3403808).

[16] P. Lin, C. Qian, Y. Jia, *et al.*, “Assembling reconfigurable intelligent metasurfaces

with a synthetic neural network,” *IEEE Transactions on Antennas and Propagation*, vol. 72, no. 6, pp. 5252–5260, 2024. DOI: [10.1109/TAP.2024.3395909](https://doi.org/10.1109/TAP.2024.3395909).

[17] S. C. Chapra and R. Canale, *Numerical Methods for Engineers*, 5th ed. USA: McGraw-Hill, Inc., 2005, ISBN: 0073101567.

[18] K. I. Gubbi, B. S. Latibari, M. A. Chowdhury, *et al.*, “Optimized and automated secure ic design flow: A defense-in-depth approach,” *IEEE Transactions on Circuits and Systems I: Regular Papers*, vol. 71, no. 5, pp. 2031–2044, 2024. DOI: [10.1109/TCSI.2024.3364160](https://doi.org/10.1109/TCSI.2024.3364160).

[19] L. Kouhalvandi and L. Matekovits, “Conjointly active and passive modelings with deep neural networks as fully automated optimizations for upper-mid band 6g communications,” *Scientific Reports*, vol. 14, no. 1, p. 17993, 2024, ISSN: 2045-2322. DOI: [10.1038/s41598-02468011-8](https://doi.org/10.1038/s41598-02468011-8). [Online]. Available: <https://doi.org/10.1038/s41598-02468011-8>.

[20] S. Yarman, “Design of ultra wideband power transfer networks,” *New York, NY, USA: Wiley*, 2010. DOI: [10.1002/9780470688922](https://doi.org/10.1002/9780470688922).

[21] L. Kouhalvandi, O. Ceylan, and S. Ozoguz, “Multi-objective efficiency and phase distortion optimizations for automated design of power amplifiers through deep neural networks,” in *2021 IEEE MTT-S International Microwave Symposium (IMS)*, 2021, pp. 233–236. DOI: [10.1109/IMS19712.2021.9574937](https://doi.org/10.1109/IMS19712.2021.9574937).

[22] L. Kouhalvandi, O. Ceylan, and S. Ozoguz, “Automated deep neural learning-based optimization for high performance high power amplifier designs,” *IEEE Transactions on Circuits and Systems I: Regular Papers*, vol. 67, no. 12, pp. 4420–4433, 2020. DOI: [10.1109/TCSI.2020.3008947](https://doi.org/10.1109/TCSI.2020.3008947).

[23] C. Frenkel, D. Bol, and G. Indiveri, “Bottom-up and top-down approaches for the design of neuromorphic processing systems: Tradeoffs and synergies between natural and artificial intelligence,” *Proceedings of the IEEE*, vol. 111, no. 6, pp. 623–652, 2023. DOI: [10.1109/JPROC.2023.3273520](https://doi.org/10.1109/JPROC.2023.3273520).

[24] L. Kouhalvandi, L. Matekovits, and I. Peter, “Deep learning assisted automatic methodology for implanted mimo antenna designs on large ground plane,” *Electronics*, vol. 11, no. 1, 2022, ISSN: 2079-9292. DOI: [10.3390/electronics11010047](https://doi.org/10.3390/electronics11010047). [Online]. Available: <https://www.mdpi.com/2079-9292/11/1/47>.

[25] F. Mir, L. Kouhalvandi, L. Matekovits, and E. O. Gunes, “Automated optimization for broadband flat-gain antenna designs with artificial neural network,” *IET Microwaves, Antennas & Propagation*, vol. 15, no. 12, pp. 1537–1544, 2021. DOI: <https://doi.org/10.1049/mia2.12137>. eprint: <https://ietresearch.onlinelibrary.wiley.com/doi/pdf/10.1049/mia2.12137>. [Online]. Available: <https://ietresearch.onlinelibrary.wiley.com/doi/abs/10.1049/mia2.12137>.